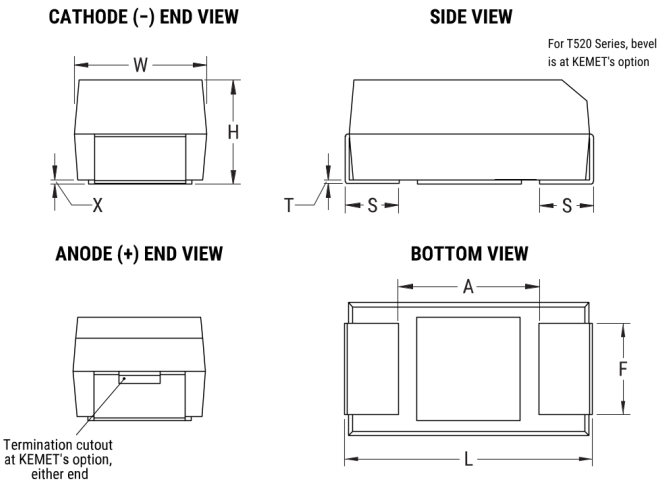
T520U107M006ATE055

T520, Tantalum, Polymer Tantalum, 100 uF, 20%, 6.3 VDC, SMD, Polymer, Molded, Low Profile/ESR, NonCombustible, 55 mOhms, 6032, Height Max = 1.5mm



Click here for the 3D model.

Dimensions

Footprint	6032
L	6mm +/-0.3mm
W	3.2mm +/-0.2mm
H	1.4mm +/-0.1mm
T	0.13mm REF
S	1.3mm +/-0.3mm
F	2.2mm +/-0.1mm
A	2.9mm MIN
X	0.05mm REF

Packaging Specifications

Weight	117.36 mg
Packaging	T&R, 178mm
Packaging Quantity	1000

General Information

Series	T520
Dielectric	Polymer Tantalum
Style	SMD Chip
Description	SMD, Polymer, Molded, Low Profile/ESR, NonCombustible
Features	Low ESR
RoHS	Yes
Termination	Tin
AEC-Q200	No
Shelf Life	52 Weeks
MSL	3

Specifications

Capacitance	100 uF
Capacitance Tolerance	20%
Voltage DC	6.3 VDC (105C)
Temperature Range	-55/+105°C
Rated Temperature	105°C
Humidity	60C, 90% RH, 500 Hours, No Load
Dissipation Factor	8% 120Hz 25C
Failure Rate	N/A
Resistance	55 mOhms (100kHz 25C)
Ripple Current	1600 mA (rms, 100kHz 45C), 1120 mA (rms, 85C), 400 mA (rms, 105C)
Leakage Current	63 uA (5min 25°C)

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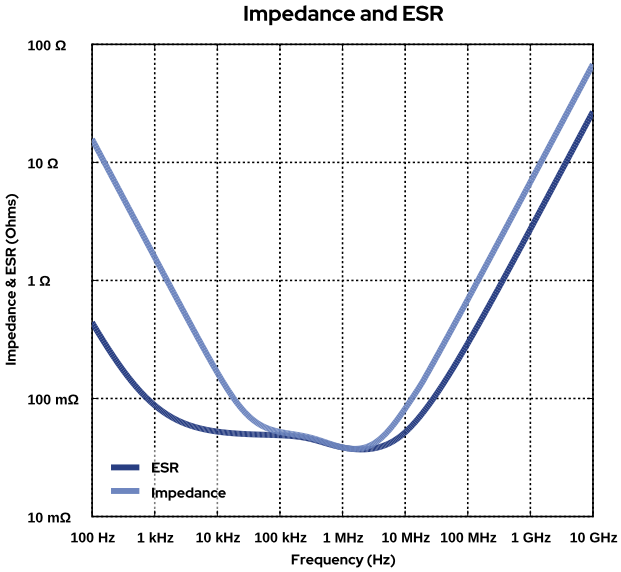
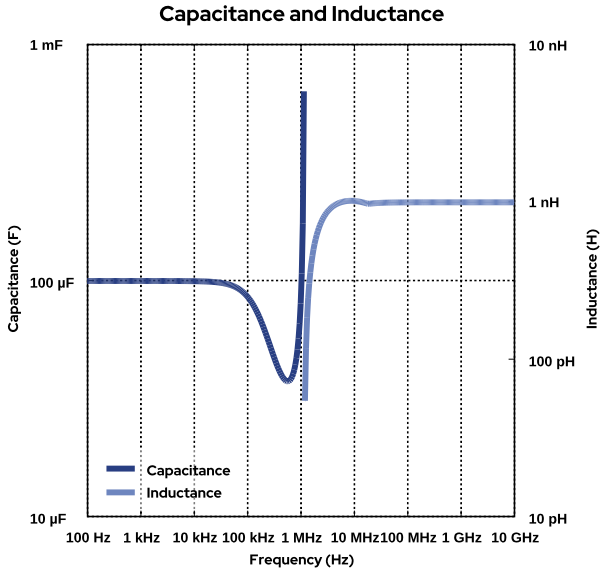
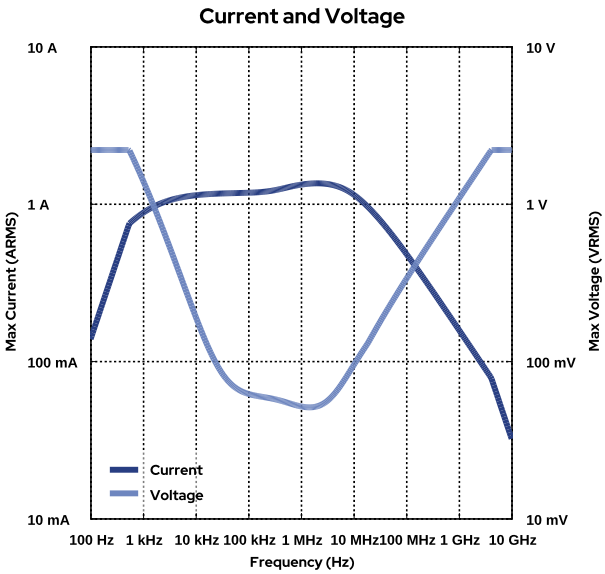
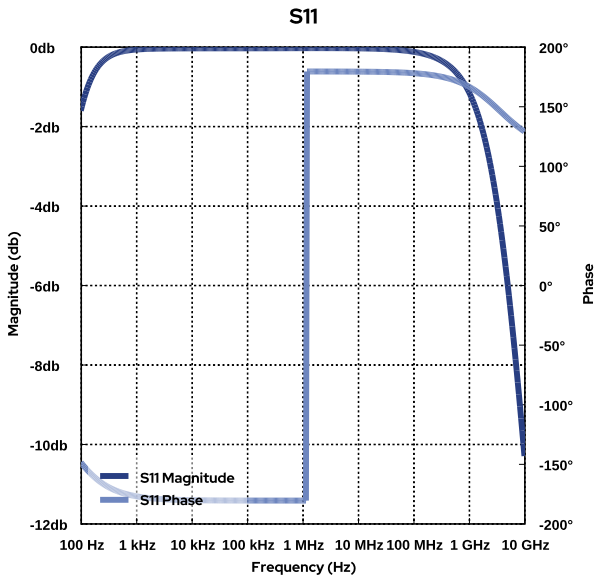
Simulations

For the complete simulation environment please visit K-SIM.









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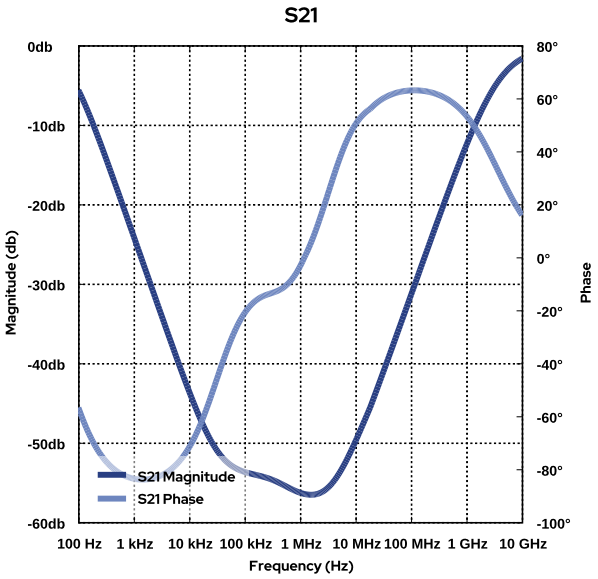

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These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
- The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.