

## **CRYSTAL OSCILLATOR (SPXO)**

**OUTPUT: LV-PECL, LVDS** 





## **Product Number**

SG2520EGN: X1G005881xxxx15 SG2520VGN: X1G005901xxxx15

# SG2520EGN SG2520VGN

•Frequency range : 25 MHz to 500 MHz

 Supply voltage : 1.8 V Typ. (LVDS only) / 2.5 V Typ. / 3.3 V Typ.

:  $\pm 25 \times 10^{-6}$ ,  $\pm 30 \times 10^{-6}$ ,  $\pm 50 \times 10^{-6}$ •Frequency tolerance •Operating temperature: -40 °C to +85 °C, -40 °C to +105 °C Output enable (OE) or Standby ( $\overline{ST}$ ) Function : 50 fs Max. (fo = 491.52 MHz) Phase jitter



 $(2.5 \times 2.0 \times 0.74 \text{ mm})$ 

#### Specifications (characteristics)

lka m		Specifications		0 15 / 15		
Item	Symbol	LV-PECL SG2520EGN	LVDS SG2520VGN		Conditions / Remarks	
Output frequency range	fo	3G2320EGN	25 MHz to 500 M		Please contact us for av	nilable fraguencies
Output frequency range	10	D: 2.5 V ± 5 %	25 MH2 to 500 M	D: 2.5 V ± 5 %	Flease contact us for av	aliable frequencies.
Supply voltage	Vcc	C: 3.3 V ± 5 %	E: 1.8 V ± 5 %	C: 3.3 V ± 5 %		
Storage temperature	T_stg		-55 °C to +125 °			
Operating temperature	T_use	G:	-40 °C to +85 °C, H: -40			
			D: ±25 x 10 <sup>-6</sup> Max.		Includes initial frequency	
Frequency tolerance	f_tol		E: ±30 × 10 <sup>-6</sup> Max. J: ±50 × 10 <sup>-6</sup> Max.		temperature characterist coefficient and 10 years	
		60 mA Max.	-		OE or $\overline{ST} = V_{CC}$ , L_ECL	= 50 Ω
Current consumption	Icc	_		25 mA / 30 mA / 25 mA Max. 28 mA / 35 mA / 28 mA Max. 28 mA / 35 mA / 30 mA Max.	OE or $\overline{ST} = V_{CC}$ , Output option: A / B / C	25 MHz ≤ fo < 212 MHz 212 MHz ≤ fo < 392 MHz 392 MHz ≤ fo ≤ 500 MHz
Disable current	I_dis	35 mA Max.	20 ו	nA Max.	OE = GND	
Otan d h	1 -4-1		30 μA Max.		ST = GND, T_use Max.	= +85 °C
Stand-by current	I_std		60 μA Max.		ST = GND, T_use Max.	= +105 °C
Symmetry	SYM		45 % to 55 %		At output crossing point	
Output voltage (LV-PECL)	V <sub>OH</sub>	V <sub>CC</sub> - 1.1 V Min. V <sub>CC</sub> - 1.5 V Max.	_		Output option: A, DC cha	aracteristic
		0.8 V to 2.0 V	500 mV to 900 mV		Output option: A	
Differential swing	$V_{SW}$	_	-	400 mV to 1 000 mV	Output option: B	
		-	600 mV	to 1 200 mV	Output option: C	
			250 mV to 450 mV		Output option: A	Differential output voltage,
	Vod	_	_	200 mV to 500 mV	Output option: B	Vod1, Vod2
Output voltage (LVDS)			300 mV to 600 mV		Output option: C	VOD1, VOD2
Output voltage (LVDS)	dV <sub>OD</sub>	-		nV Max.	$dV_{OD} =  V_{OD1} - V_{OD2} $	
	Vos	-	0.65 V to 0.85 V	1.15 V to 1.35 V	Offset voltage, Vos1, Vos	2
	dVos		50 r	nV Max.	dVos =   Vos1 - Vos2	
	L_ECL	50 Ω			Terminated to V <sub>CC</sub> - 2.0 \	
Output load condition	L LVDS	_			Output option: A, C	Connected between
	_		50 Ω		Output option: B	OUT and OUT
Input voltage	V <sub>IH</sub> V <sub>IL</sub>		70 % V <sub>CC</sub> Min.		OE or ST terminal	
·	VIL	30 % V <sub>CC</sub> Max.		LV-PECL: 20 % - 80 %	(//	
Rise/Fall times	tr/tf	0.35 ns Max.			differential output peak to peak	
Start-up time	t_str				t = 0 at 90 % Vcc	
		250 fs Max.	400 fs Max.	250 fs Max.	25 MHz ≤ fo < 100 MHz	
		90 fs Max.	130 fs Max.	100 fs Max.	100 MHz ≤ fo ≤ 156 MHz	
Phase jitter	t₽J	70 fs Max.	70 fs Max.	60 fs Max.	156 MHz < fo ≤ 212 MHz	
		60 fs Max.	60 fs Max.	50 fs Max.	212 MHz < fo ≤ 391 MHz	
		50 fs Max.	60 fs Max.	50 fs Max.	391 MHz < fo ≤ 500 MHz	z 12 kHz to 20 MHz

Product Name (Standard form)

### SG2520 EGN 156.250000MHz C D H P Z A

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①Model ②Output (E: LV-PECL, V: LVDS) ③Frequency ④Supply voltage ⑤Frequency tolerance ®Operating temperature ⑦Function ®Output disable type (Z: High impedance) ®Output option

Supply voltage			
C	3.3 V Typ.		
D	2.5 V Typ.		
E*	1.8 V Typ.		
* "E" is only for SG2520VGN			

(5) F	⑤Freq. tolerance		
D	±25 × 10 <sup>-6</sup>		
Ε	±30 × 10 <sup>-6</sup>		
J	$\pm 50 \times 10^{-6}$		

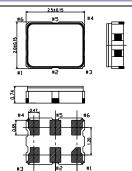
_	_	, Op -
	6	Operating temp.
	G	-40 °C to +85 °C
	Н	-40 °C to +105 °C

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(Unit:mm)

Output option			
	SG2520EGN	SG2520VGN	
Α	Default	$L_LVDS = 100 \Omega$ , $V_{OD} = 250 \text{ mV to } 450 \text{ mV}$	
В	-	$L_LVDS = 50 \Omega$ , $V_{OD} = 200 \text{ mV}$ to 500 mV	
С	-	L LVDS = $100 \Omega$ , $V_{OD} = 300 \text{ mV}$ to $600 \text{ mV}$	

#### External dimensions



Pin	map	
	Pin	Connection
	1	OE
	2	N.C. (Open or Vcc)
	3	GND
	4	OUT
	5	OUT
	6	Vcc

OE pin = HIGH or "Open": Specified frequency output. OE pin = LOW: Output is high impedance

# Footprint (Recommended) (Unit:mm) 0.995

In order to achieve optimum jitter performance, it is recommended that 0.1 µF and 10 µF bypass capacitors should be connected between  $V_{\text{CC}}$  and GND and placed as close to the V<sub>CC</sub> pin as possible.

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All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

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Explanation of the mark that are using it for the catalog



►Pb free.



► Complies with EU RoHS directive.

\*About the products without the Pb-free mark.

Contains Pb in products exempted by EU RoHS directive.





▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



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