

### **CRYSTAL OSCILLATOR (SPXO)**

**OUTPUT: LV-PECL, LVDS** 



**Product Number** SG2520EHN: X1G005921xxxx15

## SG2520EHN SG2520VHN

•Frequency range : 25 MHz to 500 MHz

 Supply voltage : 1.8 V Typ. (LVDS only) / 2.5 V Typ. / 3.3 V Typ.

:  $\pm 20 \times 10^{-6}$ •Frequency tolerance

•Operating temperature: -40 °C to +85 °C, -40 °C to +105 °C Output enable (OE) or Standby (ST) Function : 50 fs Max. (fo = 491.52 MHz) Phase jitter

 $(2.5 \times 2.0 \times 0.74 \text{ mm})$ 



#### Specifications (characteristics)

		Specifications					
Item	Symbol	LV-PECL	LVDS		Conditions / Remarks		
		SG2520EHN	SG2520VHN				
Output frequency range	fo		25 MHz to 500 MHz		Please contact us for ava	ailable frequencies.	
Supply voltage	Vcc	D: 2.5 V ± 5 % C: 3.3 V ± 5 %	E: 1.8 V ± 5 %	D: 2.5 V ± 5 % C: 3.3 V ± 5 %			
Storage temperature	T_stg		-55 °C to +125 °				
Operating temperature	T_use	G:	-40 °C to +85 °C, H: -40	°C to +105 °C			
Frequency tolerance	f_tol	C: ±20 × 10 <sup>-6</sup> Max.		Includes initial frequency temperature characterist coefficient and 10 years	ics, frequency / voltage aging (+25 °C)		
		60 mA Max.	_		OE or $\overline{ST} = V_{CC}$ , L_ECL		
Current consumption	Icc	_	25 mA / – / 25 mA Max.	25 mA / 30 mA / 25 mA Max. 28 mA / 35 mA / 28 mA Max. 28 mA / 35 mA / 30 mA Max.	OE or $\overline{ST} = V_{CC}$ , Output option: A / B / C	25 MHz ≤ fo < 212 MHz 212 MHz ≤ fo < 392 MHz 392 MHz ≤ fo ≤ 500 MHz	
Disable current	I dis	35 mA Max.	20.1	mA Max.	OE = GND	002 1111 12 = 10 = 000 1111 12	
			30 µA Max.		ST = GND, T use Max. = +85 °C		
Stand-by current	I_std		60 μA Max.		ST = GND. T use Max.		
Symmetry	SYM		* *		At output crossing point		
Output voltage (LV-PECL)	V <sub>OH</sub> V <sub>OL</sub>	V <sub>CC</sub> - 1.1 V Min. V <sub>CC</sub> - 1.5 V Max.	n.		Output option: A, DC characteristic		
		0.8 V to 2.0 V	500 m\	/ to 900 mV	Output option: A		
Differential swing	$V_{SW}$	_	_	400 mV to 1 000 mV	Output option: B		
		_		to 1 200 mV	Output option: C		
	V <sub>OD</sub>	_	250 m\	/ to 450 mV	Output option: A	Differential output voltage,	
			<ul> <li>200 mV to 500 mV</li> </ul>		Output option: B	V <sub>OD1</sub> , V <sub>OD2</sub>	
Output voltage (LVDS)			300 mV to 600 mV		Output option: C	V OD1, V OD2	
Output voltage (EVDO)	dVod	_			$dV_{OD} =  V_{OD1} - V_{OD2} $		
	Vos	-	0.65 V to 0.85 V	1.15 V to 1.35 V	Offset voltage, Vos1, Vos2		
	dVos	-	50 mV Max.		$dV_{OS} =  V_{OS1} - V_{OS2} $		
Output load condition	L_ECL	50 Ω	_		Terminated to V <sub>CC</sub> - 2.0 \		
	L_LVDS	_		100 Ω	Output option: A, C Connected between		
			- 50 Ω		Output option: B	OUT and OUT	
Input voltage	V <sub>IH</sub>		70 % V <sub>CC</sub> Min.		OE or ST terminal		
	V <sub>IL</sub>	30 % V <sub>CC</sub> Max.					
Rise/Fall times	tr/tf			LV-PECL: 20 % - 80 % LVDS: 20 % - 80 %	(V <sub>OH</sub> - V <sub>OL</sub> ) differential output peak to peak		
Start-up time	t_str		10 ms Max.		t = 0 at 90 % Vcc		
	tpJ	250 fs Max.	400 fs Max.	250 fs Max.	25 MHz ≤ fo < 100 MHz	Offset frequency	
Phase jitter		90 fs Max.	130 fs Max.	100 fs Max.	100 MHz ≤ fo ≤ 156 MHz		
		70 fs Max.	70 fs Max.	60 fs Max.	156 MHz < fo ≤ 212 MHz	z 12 kHz to 5 MH	
		60 fs Max.	60 fs Max.	50 fs Max.	212 MHz < fo ≤ 391 MHz		
		50 fs Max.	60 fs Max.	50 fs Max.	391 MHz < fo ≤ 500 MHz	z 12 kHz to 20 MH	

Product Name (Standard form) SG2520 EHN 156.250000MHz C C H P Z A 456789

 $\textcircled{9} \textbf{Model} \quad \textcircled{2} \textbf{Output (E: LV-PECL, V: LVDS)} \quad \textcircled{3} \textbf{Frequency} \quad \textcircled{4} \textbf{Supply voltage} \quad \textcircled{5} \textbf{Frequency tolerance (C: $\pm 20 \times 10^{-6})}$ 

4)	Supply voltage			
С	3.3 V Typ.			
D	2.5 V Typ.			
E*	1.8 V Typ.			
* "E" is only for SG2520VHN				

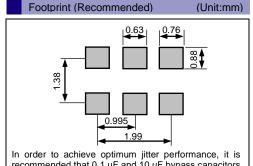
⑥Operating temp.				
G	-40 °C to +85 °C			
Н	-40 °C to +105 °C			

(	⑦F	unction
	Р	OE
	S	S₹

OE pin = LOW: Output is high impedance

90	Output option				
	SG2520EHN	SG2520VHN			
Α	Default	$L_LVDS = 100 \Omega$ , $V_{OD} = 250 \text{ mV}$ to 450 mV			
В	-	$L_LVDS = 50 \Omega$ , $V_{OD} = 200 \text{ mV}$ to $500 \text{ mV}$			
С	i	$L_LVDS = 100 \Omega$ , $V_{OD} = 300 \text{ mV}$ to $600 \text{ mV}$			

### External dimensions (Unit:mm) Pin map Pin Connection OE N.C. (Open or Vcc) GND 4 OUT 5 OUT OE pin = HIGH or "Open": Specified frequency output.



In order to achieve optimum jitter performance, it is recommended that 0.1  $\mu F$  and 10  $\mu F$  bypass capacitors should be connected between  $V_{CC}$  and GND and placed as close to the V<sub>CC</sub> pin as possible.

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►Pb free.



► Complies with EU RoHS directive.

\*About the products without the Pb-free mark.

Contains Pb in products exempted by EU RoHS directive.





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